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DEC 0 1 2009

File No.: 17273-6US - AD/ad

Montréal, Canada,
December 1st, 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Richard S. Norman
Serial Number:	10/022,851
Filing Date:	December 20, 2001
Title:	METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS
Agent of Record:	Alexandra Daoud – Tel. (514) 847-4333

BY FAX: 571-273-8300

Commissioner of Patents
U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450
U.S.A.

RENEWED PETITION FOR REVIVAL OF AN APPLICATION FOR PATENT ABANDONED UNINTENTIONALLY UNDER 37 CFR 1.137(b)

Sir:

This is in response to your letter dated November 12, 2009.

Please find enclosed a Petition for Revival of an Application for Patent Abandoned Unintentionally duly completed for reinstatement of the above-mentioned application. Included herewith are the following items:

- Statement of facts from Assignee
- Statement of facts from Former Counsel
- Statement of facts from Present Counsel

indicating that the entire delay in filing the required reply from the due date for the reply until the filing of a grantable petition was unintentional. The delay in reply that originally resulted in the abandonment is shown to result unintentionally as a change of correspondence address was submitted to the USPTO but never recorded.

Also enclosed are:

- Response to outstanding Office Action dated September 6, 2005
- Power of Attorney signed by Assignee

Favourable consideration on the merits of this application is earnestly solicited.

Respectfully submitted,

Richard S. Norman

By: _____
Alexandra Daoud (Reg. No. 55,992)
Agent of Record
Ogilvy Renault LLP
Customer Number 020988

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Doc Code: PET.OP
Document Description: Petition for Review by the Office of Petitions

PTO/SB/64 (04-09)
Approved for use through 05/31/2009. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

PETITION FOR REVIVAL OF AN APPLICATION FOR PATENT ABANDONED UNINTENTIONALLY UNDER 37 CFR 1.137(b)	Docket Number (Optional) 17273-6US - AD

First named inventor: Claude THIBEAULT

Application No.: 10/022,851 Art Unit: 2631

Filed: December 20, 2001 Examiner: Qutbuddin GHULAMALI

Title: METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS

Attention: Office of Petitions
Mail Stop Petition
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
FAX (571) 273-8300

NOTE: If information or assistance is needed in completing this form, please contact Petitions Information at (571) 272-3282.

The above-identified application became abandoned for failure to file a timely and proper reply to a notice or action by the United States Patent and Trademark Office. The date of abandonment is the day after the expiration date of the period set for reply in the office notice or action plus any extensions of time actually obtained.

APPLICANT HEREBY PETITIONS FOR REVIVAL OF THIS APPLICATION

NOTE: A grantable petition requires the following items:
(1) Petition fee;
(2) Reply and/or issue fee;
(3) Terminal disclaimer with disclaimer fee - required for all utility and plant applications filed before June 8, 1995; and for all design applications; and
(4) Statement that the entire delay was unintentional

1. Petition Fee

☑ Small entity-fee $ 810.00 (37 CFR 1.17(m)). Application claims small entity status. See 37 CFR 1.27.

☐ Other than small entity-fee $ _____ (37 CFR 1.17(m))

2. Reply and/or fee

A. The reply and/or fee to the above-noted Office action in the form of an Amendment (identify type of reply):

☐ has been filed previously on _____.

☑ is enclosed herewith.

B. The issue fee and publication fee (if applicable) of $_____.

☐ has been paid previously on _____.

☐ is enclosed herewith.

This collection of information is required by 37 CFR 1.137(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Mail Stop Petition, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

PTO/SB/64 (04-09)
Approved for use through 05/31/2009. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

3. Terminal disclaimer with disclaimer fee

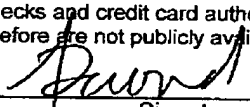
☑ Since this utility/plant application was filed on or after June 8, 1995, no terminal disclaimer is required.

☐ A terminal disclaimer (and disclaimer fee (37 CFR 1.20(d)) of $_____ for a small entity or $_____ for other than a small entity) disclaiming the required period of time is enclosed herewith (see PTO/SB/63).

4. STATEMENT: The entire delay in filing the required reply from the due date for the required reply until the filing of a grantable petition under 37 CFR 1.137(b) was unintentional. [NOTE: The United States Patent and Trademark Office may require additional information if there is a question as to whether either the abandonment or the delay in filing a petition under 37 CFR 1.137(b) was unintentional (MPEP 711.03(c), subsections (III)(C) and (D)).]

WARNING:

Petitioner/applicant is cautioned to avoid submitting personal information in documents filed in a patent application that may contribute to identity theft. Personal information such as social security numbers, bank account numbers, or credit card numbers (other than a check or credit card authorization form PTO-2038 submitted for payment purposes) is never required by the USPTO to support a petition or an application. If this type of personal information is included in documents submitted to the USPTO, petitioners/applicants should consider redacting such personal information from the documents before submitting them to the USPTO. Petitioner/applicant is advised that the record of a patent application is available to the public after publication of the application (unless a non-publication request in compliance with 37 CFR 1.213(a) is made in the application) or issuance of a patent. Furthermore, the record from an abandoned application may also be available to the public if the application is referenced in a published application or an issued patent (see 37 CFR 1.14). Checks and credit card authorization forms PTO-2038 submitted for payment purposes are not retained in the application file and therefore are not publicly available.



Signature — December 1st, 2009 (Date)

Alexandra Daoud (Type or Printed name) — 55992 (Registration Number, If applicable)

1 Place Ville-Marie, Suite 2500 (Address) — 514.847.4333 (Telephone Number)

Montreal, Quebec, H3B 1R1, CANADA (Address)

Enclosures:

☑ Fee Payment

☑ Reply

☐ Terminal Disclaimer Form

☑ Additional sheets containing statements establishing unintentional delay

☑ Other: Power of Attorney

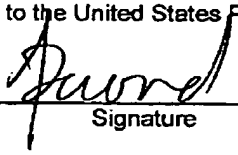
CERTIFICATE OF MAILING OR TRANSMISSION [37 CFR 1.8(a)]

I hereby certify that this correspondence is being:

☐ Deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Petition, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

☑ Transmitted by facsimile on the date shown below to the United States Patent and Trademark Office at (571) 273-8300.

December 1st, 2009
Date



Signature

ALEXANDRA DAOUD
Typed or printed name of person signing certificate

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File No.: 17273-6US

Montreal, Canada

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Karl FECTEAU et al.
Assignee :	Richard S. Norman
Serial Number:	10/022,851
Filing Date:	December 20, 2001
Title:	METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS
Agent of Record:	Alexandra Daoud – Tel. (514) 847-4333

Statement of Facts by Assignee

Sir,

I, Richard S. Norman, do hereby declare that:

I am a Canadian citizen residing at 1877 Chemin Poissant, Sutton, Québec, Canada J0E 2K0.

I am the assignee of the above-reference application, as evidenced by the document recorded at the USPTO at Reel 015408, Frame 0178.

I was contacted on May 21, 2009 by Ms. Alexandra Daoud, my Patent Counsel, and informed that the present application had been abandoned for failure to respond to an Office Action.

On May 27, 2009, I confirmed to Ms. Daoud that this was the first I had heard of the Office Action for this application and that we had no intention of abandoning the application.

I asked that the application be reinstated.

Any delay in responding to the first Office Action of the present application was unintentional on my part.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application of any patent issued thereon.

SIGNATURE Richard Norman DATE Sept. 3, 2009

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DEC 01 2009

File No.: 17273-6US

Montreal, Canada

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Karl FECTEAU et al.
Assignee :	Richard S. Norman
Serial Number:	10/022,851
Filing Date:	December 20, 2001
Title:	METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS
Agent of Record:	Alexandra Daoud – Tel. (514) 847-4333

Statement of Facts by Agent of Record

Sir,

I, Alexandra Daoud, do hereby declare that:

I am a Canadian citizen residing at 4215 Oakland, Brossard, Québec, J4Y 0A4, Canada.

I am a registered patent agent at the USPTO under registration number 55,992.

I am a partner of the firm Swabey Ogilvy Renault.

On May 20, 2009, we inadvertently discovered that correspondence for the above-referenced patent application had been issued on August 25, 2005 and January 18, 2006 and we had no record of it in our files.

Upon further inquiry, we discovered that the request for a change of agent and change of correspondence in this matter, received by the USPTO on August 6, 2003 as evidenced by appendix A, was never made of record.

On August 6, 2003, a correspondence was sent to the previous agents of record indicating that we were revoking the power of attorney and appointing a new agent, and requesting that any future correspondence received by them be forwarded to our attention.

After this date, no correspondence was sent to us by the previous agents of record, despite our request.

The Office Action dated September 6, 2005 and the Notice of Abandonment dated April 19, 2006 were transmitted to the previous agents of record, as evidenced by the correspondence address indicated on these communications.

The following day that the unintentional abandonment was discovered (May 21, 2009), we communicated with the Assignee, Richard S. Norman, to inform him of the situation.

On May 27, 2009, Mr. Norman confirmed that this was the first he had heard of the Office Action for this application and that they had no intention of abandoning the application. Mr. Norman asked that the application be reinstated.

In the time that has lapsed since discovering the abandonment, we have obtained signed statements of facts from the Assignee and the Former Counsel, the latter of which was received on September 4, 2009, in order to meet the requirements for a Petition for Revival of an Application for Patent Abandoned Unintentionally under 37 CFR 1.137(b).

Therefore, I hereby submit that the entire delay in filing the required reply from the due date for the reply until the filing of a grantable petition was unintentional.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application of any patent issued thereon.

SIGNATURE _____ DATE 09-09-09

Attorney Docket: 038700-0276792

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE PATENT APPLICATION OF	:	Claude THIBEAULT *et al.*
SERIAL NO.	:	10/022,851
FILING DATE	:	December 20, 2001
ART UNIT	:	2637
EXAMINER	:	QUTBUDDIN GHULAMALI
FOR	:	METHODS, APPARATUS, AND SYSTEMS FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS

STATEMENT OF FACTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby declare as follows:

1. I have reviewed our firm's records pertaining to the above-identified application. Relevant copies of these records are attached! Our records show the following:

a. We filed the above-captioned patent application on December 20, 2001.

b. The above-captioned patent application was assigned to Hyperchip, Inc. by means of an assignment document recorded with the U.S. Patent and Trademark Office on March 18, 2002, at Reel/Frame 012697/0399 (See Exhibit A).

c. We were instructed by Mr. C. Marc Benoit, IP Manager, of Hyperchip, Inc. that Hyperchip would be handling future prosecution of the above-captioned application and were asked to return the files to Hyperchip, Inc. (See Exhibit B).

d. On or about July 25, 2002, we forwarded the file for the above-captioned patent application to Hyperchip, Inc. (See Exhibit C).

e. An Office Action dated September 6, 2005, was received in our office on September 7, 2005 (See Exhibit D).

f. On September 12, 2005, we forwarded the Office Action to Hyperchip, Inc. by First-Class Mail to our last known address of record (See Exhibit E).

g. Our letter of September 12, 2005 to Hyperchip, Inc., was subsequently returned to us (See Exhibit F).

h. We subsequently received a Notice of Abandonment, dated April 19, 2006, for the above-captioned application (See Exhibit G).

i. On April 21, 2006, we forwarded the Notice of Abandonment to Hyperchip, Inc. by First-Class Mail (See Exhibit H).

j. Our letter of April 21, 2006 was returned to us (See Exhibit I).

2. I hereby acknowledge that willful false statements and the like are punishable by fine or imprisonment, or both (18 U.S.C. § 1001) and may jeopardize the validity of the present application or any patent issuing thereon. All statements made of my knowledge are true and all statements made on information and belief are believed to be true.

Respectfully submitted,
PILLSBURY WINTHROP SHAW PITTMAN LLP

By: _____
Bryan P. Collins
Registration No. 43,560
Tel. No. 703.770.7538
Fax No. 703.770.7901

Date: 9/31/09
P.O. Box 10500
McLean, VA 22102
703.770.7900

EXHIBIT A

United States Patent and Trademark Office

Assignments on the Web > Patent Query

Patent Assignment Abstract of Title

NOTE:Results display only for issued patents and published applications. For pending or abandoned applications please consult USPTO staff.

Total Assignments: 3

Patent #: NONE **Issue Dt:** **Application #:** 10022851 **Filing Dt:** 12/20/2001

Publication #: 20030117183 **Pub Dt:** 06/26/2003

Inventors: Claude Thibeault, Karl Fecteau, Jean-Jacques Laurin, Yvon Savaria, Zhong-Fang Jin

Title: Methods, apparatus, and systems for reducing interference on nearby conductors

Assignment: 1

Reel/Frame: 012697/0399 **Recorded:** 03/18/2002 **Pages:** 4

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignors: THIBEAULT, CLAUDE **Exec Dt:** 03/15/2002

FECTEAU, KARL **Exec Dt:** 03/14/2002

LAURIN, JEAN-JACQUES **Exec Dt:** 03/11/2002

SAVARIA, YVON **Exec Dt:** 03/14/2002

JIN, ZHONG-FANG **Exec Dt:** 03/14/2002

Assignee: HYPERCHIP INC.
1800 BOUL. RENE-LEVESQUE OUEST
MONTREAL, QUEBEC, CANADA H3H 2

Correspondent: PILLSBURY WINTHROP LLP
KERRY HARTMAN
1600 TYSONS BOULEVARD
MCLEAN, VA 22102

Assignment: 2

Reel/Frame: 015408/0178 **Recorded:** 11/24/2004 **Pages:** 5

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: 4198638 CANADA INC **Exec Dt:** 10/14/2004

Assignee: NORMAN, RICHARD S.
1877 CHEMIN POISSANT SUTTON
QUEBEC, CANADA J0E 2K0

Correspondent: OGILVY RENAULT
C. MARC BENOIT
1981 MCGILL COLLEGE AVENUE, SUITE 1600
MONTREAL, CANADA H3AZY-3

Assignment: 3

Reel/Frame: 015408/0203 **Recorded:** 11/24/2004 **Pages:** 8

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HYPERCHIP INC. **Exec Dt:** 10/14/2004

Assignee: 4198638 CANADA INC.
4028 MARLOWE
MONTREAL, QUEBEC, CANADA H4A 3M2

Correspondent: 4198638 CANADA INC.
C. MARC BENOIT
4028 MARLOWE, MONTREAL, QUEBEC

PAGE 12/47 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

CANADA, H4A3M2

Search Results as of: 08/26/2009 08:02 PM
If you have any comments or questions concerning the data displayed, contact PRD / Assignments at 571-272-3350.
Web interface last modified: October 18, 2008 v.2.0.2

EXHIBIT B

HYPERCHIP
THE PETABIT ROUTING COMPANY

Montreal, May 31, 2001

Via Courier

Mr. Jeffrey Karceski
PILLSBURY WINTHROP
1600 Tysons Blvd.,
McLean, VA 22102

Re: Transfer of Hyperchip files
Your/our refs: see attached list

Dear Jeff,

We regret to inform you that, for cost and efficiency reasons, Hyperchip needs to bring as much intellectual property work in-house as possible.

Accordingly, effective immediately, Hyperchip will be handling drafting, prosecution and maintenance of the patent files on the attached list.

Please coordinate the return of all material and work in progress associated with these files with the undersigned. Also, kindly advise us before incurring any further costs.

We thank you for your professional services and hope to have further dealings with you in the future.

Best regards,

C. Marc Benoît
IP Manager

CMB/al

Enclosure: List of references

Cc: Kerry Hartman

1800 René-Lévesque blvd. W., Montréal, Québec H3H 2H2
T 514.906.2447 F 514.906.2500 www.hyperchip.com

List of Hyperchip's Files

Pillsbury File #	HC File #	Application No.	Title	Status
276792	P(US)2001-019	US 10/022,851	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
283335	P(US)2001-086		Synchronization of Data Transfers	In Drafting
290547	P(US)2001-199	US 10/022,856	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
290548	P(US)2001-200	US 10/022,852	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam
290549	P(US)2001-201	US 10/023,478	Methods, Apparatus and Systems for Reducing Interference on Nearby Conductors	Pending In-Exam

Confidential Hyperchip Document, © 2002 Hyperchip Inc., all rights reserved.

12/01/2009 11:11 FAX 0016/0049

PAGE 16/47 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

EXHIBIT C

38700. Hyperchip

PILLSBURY WINTHROP LLP

1600 TYSONS BOULEVARD MCLEAN, VA 22102 703.905.2000 F: 703.905.2500

Jeffrey A. Cox
703.905.2048
jcox@pillsburywinthrop.com

July 25, 2002

~~LegalKey Processed~~

LegalKey Processed
Re-updated 07-10-03
Gina Berrios
LegalKey Processed

Mr. Marc Benoit
Hyperchip Inc.
1800 boul. Rene'-Levesque, ouest
Montreal, Quebec H3H 2H2
Canada

Re: Hyperchip - ~~037800~~ 38700 2M

Dear Mr.Benoit:

Pursuant to instructions from the client, we are transferring the following trademark and/or patent file(s) to you herewith:

Matter No.	Appln. Serial No.	Country
276792 ✓	10/022,851	US
290549 ✓	10/023,478	US
290548 ✓	10/022,852	US
290547 ✓	10/022,856	US
283335 ✓		US

You are advised that Pillsbury Winthrop LLP assumes no further responsibility for these files including, but not limited to, future prosecution and\or annuities. Please file the appropriate documents to ensure that future correspondence is sent directly to you. **A docket sheet of all relevant due dates is also attached (if applicable).**

30298217v5

PILLSBURY WINTHROP

These files are being transferred with the understanding that access will be provided to Pillsbury Winthrop LLP should the need arise at a future date. Please sign and return a copy of this letter to indicate your agreement and receipt of the above files.

Very truly yours,

Jeffrey A. Cox
Records Manager

znm
Enclosures
cc: Hyperchip

PAGE 19/47 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

EXHIBIT D

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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,851	12/20/2001	Claude Thibeault	P 27679Z P(US)2001-019	1516

909 7590 09/06/2005

PILLSBURY WINTHROP SHAW PITTMAN, LLP
P.O. BOX 10500
MCLEAN, VA 22102

EXAMINER	
GHULAMALI, QUTBUDDIN	
ART UNIT	PAPER NUMBER
2637	

DATE MAILED: 09/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

RECEIVED
PILLSBURY WINTHROP SHAW PITTMAN

SEP 0 7 2005

CL 038700 MT# 027679Z
ATTY(S) SDK, KTH
DUE:
DKT BY(1) DB (2)

PTO 90C (Rev 10/03)

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Office Action Summary	Application No.	Applicant(s)	
	10/022,851	THIBEAULT ET AL.	
	Examiner	Art Unit	
	Qutub Ghulamali	2637	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *20 December 2001*.

2a)☐ This action is FINAL. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-34* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☒ Claim(s) *12-20* is/are allowed.

6)☒ Claim(s) *1-11 and 21-34* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *5/30/02*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the reference to state transitions "T1" and "T2" in the specification page 4, sections 00016, 00061) must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

3. Claims 1-11 and 21-34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Balakrishnan et al (USP 5,101,347) in view of Tan et al (USP 5,815,031).

Regarding claims 1 and 21, Balakrishnan discloses a system (transmitter and receiver) of data transmission comprising:

receiving a plurality of sets of input signals (multiple data slices), each input signal having a series of state transitions (figs. 2-3; series of data characters) synchronized to a data clock signal having a period T CLK (col. 3, lines 13-48); and

transmitting a corresponding plurality of sets of output signals such that each output signal (1) corresponds to an input signal of the corresponding set (col. 3, lines 13-25; col. 4, lines 33-40), (2) passes along a corresponding one of a plurality of conductive paths (parallel data bus), and (3) has a series of state transitions corresponding to the series of state transitions of the corresponding input signal (col. 3, lines 35-60), wherein a time between a state transition on an input signal of one set and the corresponding state transition on the corresponding output signal exceeds a time between a state transition on an input signal of another set and the corresponding state transition on the corresponding output signal by a delay period T DLY, and

wherein the period T CLK is greater than the delay period T DLY (col. 2, lines 10-39). Balakrishnan, however, does not explicitly disclose, "adjacent conductive paths that each carry an output signal of one set are separated by at least one conductive path that carries an output signal of another set". Tan in a similar field of endeavor discloses an improved signal routing scheme includes a plurality of dynamic signal lines disposed in parallel to each other wherein adjacent conductive paths that each carry an output signal of one set are separated by at least one conductive path that carries an output signal of another set (col. 2, lines 32-44, 53-60). It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use plurality of dynamic signal lines parallel to each other (adjacent conductive paths) separated by conductive path to carry output signal of another set as taught by Tan in the system of Balakrishnan because it can mitigate crosstalk noise by reducing unwanted coupling between signal paths.

Regarding claims 2, 30, Balakrishnan discloses the delay period T_DLY is at least as long as a rise time of the data clock signal (fig. 3).

Regarding claims 3 and 22, Balakrishnan discloses each state transition of an output signal corresponds to a different one among the state transitions of the corresponding input signal (col. 3, lines 13-28).

Regarding claims 4, 23 and 33, Balakrishnan discloses the plurality of conductive paths can be fabricated on a semiconductor substrate, and wherein said receiving and said transmitting occur on the semiconductor substrate (col. 4, lines 3-18).

Regarding claims 5, 6, 7, 24, 25, 26 the parameters; length, distance between adjacent conductive path, and width of the conductive path are design issues (features) related to

achieving the desired delay in a high speed signal communication and well known in semiconductor fabrication industry for achieving desired objectives in reducing the skew effects between parallel data paths, such as disclosed by Nomura (USP 6,128,347) (figs. 16, 17A-C).

Regarding claims 8 and 28, Balakrishnan discloses each one among the plurality of conductive paths includes a corresponding one of a plurality of parallel transmission lines (col. 3, lines 35-60); and

wherein said method further comprises coupling the data clock signal to one of the plurality of transmission lines (fig. 2; col. 3, lines 22-28).

Regarding claims 9, 29, Balakrishnan discloses each one among the plurality of conductive paths includes a corresponding one of a plurality of buffers (storage locations) (col. 3, lines 35-48).

Regarding claims 10, 31, Balakrishnan discloses,

transmitting each output signal among a first one of the plurality of sets of output signals includes latching the series of state transitions of the corresponding input signal onto the output signal in response to a first clock signal, and wherein transmitting each output signal among a second one of the plurality of sets of output signals includes latching the series of state transitions of the corresponding input signal onto the output signal in response to a second clock signal (fig. 2, col. 3, lines 13-48, 54-67).

Regarding claims 11 and 32, Balakrishnan discloses the first and second clock signals are based on the data clock signal, and wherein a time between a state transition on the data clock signal and a corresponding state transition on the second clock signal exceeds a time between a

state transition on the data clock signal and a corresponding state transition on the first clock signal by the delay period T DLY (col. 2, lines 10-39).

Regarding claim 34, Balakrishnan discloses a data receiver configured and arranged to receive the plurality of first output signals and the plurality of second output signals and to produce a plurality of first received signals and a plurality of second received signals (col. 4, lines 20-54), wherein each among the plurality of first received signals corresponds to one among the plurality of first output signals, and each among the plurality of second received signals corresponds to one among the plurality of second output signals (col. 3, lines 13-25; col. 4, lines 33-40), and wherein each of the first and second received signals has a series of state transitions corresponding to the series of state transitions of the corresponding output signal (col. 2, lines 10-39), and wherein the state transitions of each of the first and second received signals are synchronized to a received data clock signal, and wherein one among the rising and falling edges of the data clock signal is synchronous with the other among the rising and falling edges of the received data clock signal (col. 3, lines 61-67).

Allowable Subject Matter

4. Claims 12-20 allowed.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

US Patents:

Nomura (USP 6,128,347) discloses signal transmission circuit with protection line driven with signal having same phase as transmission signal to mitigate crosstalk.

Schipper et al (USP 6,128,337) shows a multi-path signal discrimination method.

Lincoln (US Pub. 2005/0069041) shows a coherent expandable high speed interface.

Umemura et al (USP 6,600,790) discloses a gap-coupling bus system.

Malerevich et al (USP 6,611,538) shows a data transmission synchronization system.

Pitroda et al (USP 4,627,047) discloses an integrated voice and data telecommunication switching system.

US Publications:

Bishop, J.A.; Hashemi, M.M.; Kiziloglu, K.; Larson, L.; Dagli, N.; Mishra, U. "Monolithic coaxial transmission lines for mm-wave Ics", High Speed Semiconductor Devices and Circuits, Proceedings IEEE, 5-7 August 1991 Page(s) 252 – 260.

Mikazuki, T.; Matsui, N., "Statistical design techniques for high-speed circuit boards with correlated structure distributions", Components, Hybrids, and Manufacturing Technology, IEEE Transactions on, Volume 14, Issue 3, Sept. 1991 Page(s) 512 – 517.

Mizuno, M. et al; "Clock distribution networks with on-chip transmission lines", Interconnect Technology Conference, Proceedings IEEE 2000 International, 5-7 June 2000 Page(s) 3 – 5.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Qutub Ghulamali whose telephone number is (571) 272-3014. The examiner can normally be reached on Monday-Friday from 8:00AM - 5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on (571) 272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

QG.
August 31, 2005.

JAY K. PATEL
SUPERVISORY PATENT EXAMINER

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FORM PTO-1449 (modified)
To: U.S. Department of Commerce
(PW FORM PAT-1449)
Patent and Trademark Office

Atty. Dkt. No.	M#	Client Ref.
	0276792	P(US)2001-019

Applicant: THIBEAULT et al.

Appln. No.: 10/022,851

Filing Date: December 20, 2001

Examiner: Q. G. Group Art Unit: ~~2631~~ 2637

O I P E
MAY 30 2002
PATENT & TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Date: May 30, 2002 Page 1 of 1

U.S. PATENT DOCUMENTS

Examiner's Initials*		Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
QG	AR	5,101,347	03/1992	BALAKRISHNAN et al.			
QG	BR	5,815,031	09/1998	TAN et al.			
QG	CR	5,886,943	03/1999	SEKIGUCHI et al.			
QG	DR	5,892,981	04/1999	WIGGERS			
QG	ER	5,994,766	11/1999	SHENOY et al.			
QG	FR	5,994,946	11/1999	ZHANG			
QG	GR	6,008,705	12/1999	GHOSHAL			
QG	HR	6,015,300	01/2000	SCHMIDT, JR. et al.			
QG	IR	6,081,146	06/2000	SHIOCHI et al.			
QG	JR	6,110,221	08/2000	PAI et al.			
QG	KR	6,114,890	09/2000	OKAJIMA et al.			
QG	LR	6,184,702 B1	02/2001	TAKAHASHI et al.			

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Technology Center 2600

FOREIGN PATENT DOCUMENTS

		Document Number	Date MM/YYYY	Country	Inventor Name		English Abstract Enc	English Abstract No	Translation Readily Available Enc	Translation Readily Available No

OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

QG	MR	Ismail et al., Repeater Insertion in Tree Structured Inductive Interconnect, Proceedings of the 1999 International Conference on Computer-aided Design, November 1999, pp. 420-424.
QG	NR	Ismail et al., Effects of Inductance on the Propagation Delay and Repeater Insertion in VLSI Circuits, Proceedings of the 36th ACM/IEEE Conference on Design Automation Conference, June 1999, 4 pages.
QG	OR	Alpert et al., Buffer Insertion With Accurate Gate and Interconnect Delay Computation, Proceedings of the 36th ACM/IEEE Conference on Design Automation Conference, June 1999, 6 pages.
QG	PR	Alpert et al., Buffer Insertion for Noise and Delay Optimization, Proceedings of the 35th annual conference on Design Automation Conference, May 1998, pp. 362-367
QG	QR	Davari et al., CMOS Scaling for High Performance and Low Power - The Next Ten Years, Proceedings of the IEEE, vol. 83, No. 4, April 1995, pp. 595-606.
QG	RR	Nose et al., Two Schemes to Reduce Interconnect Delay in Bi-directional and Uni-directional Buses, 2001 Symposium on VLSI Circuits, Digest of Technical Papers, pp. 193-194.
QG	SR	PCB Design Guidelines for Reduced EMI, Texas Instruments, SZZA009, November 1999, pp. i-iv and 1-19.
QG	TR	Sato et al., A 5-Gbyte/s Data-Transfer Scheme With Bit-to-Bit Skew Control For Synchronous DRAM, IEEE Journal of Solid State Circuits, vol. 34, No. 5, May 1999, pp. 653-660.

Examiner [signature] Date Considered: 7/14/05

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

Notice of References Cited	Application/Control No. 10/022,851	Applicant(s)/Patent Under Reexamination THIBEAULT ET AL.	
	Examiner Qutub Ghulamali	Art Unit 2637	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,128,347 A	10-2000	Nomura, Masahiro	375/257
	B	US-6,128,337 A	10-2000	Schipper et al.	375/229
	C	US-2005/0069041 A1	03-2005	Lincoln, Daniel J.	375/257
	D	US-6,600,790 B1	07-2003	Umemura et al.	375/257
	E	US-5,815,031 A	09-1998	Tan et al.	327/551
	F	US-5,101,347 A	03-1992	Balakrishnan et al.	710/106
	G	US-6,611,536	08-2003	Malerevich et al.	370/503
	H	US-4,627,047	12-1986	Pitroda et al.	370/384
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bishop, J.A.; Hashemi, M.M.; Kizilogiu, K.; Larson, L.; Dagli, N.; Mishra, U. "Monolithic coaxial transmission lines for mm-wave ics"; High Speed Semiconductor Devices and Circuits, Proceedings IEEE, 5-7 August 1991 Page(s) 252 - 260
	V	Mikazuki, T.; Matsui, N., "Statistical design techniques for high-speed circuit boards with correlated structure distributions", Components, Hybrids, and Manufacturing Technology, IEEE Transactions on, Volume 14, Issue 3, Sept. 1991 Page(s) 512 - 517.
	W	Mizuno, M. et al; "Clock distribution networks with on-chip transmission lines", Interconnect Technology Conference, Proceedings IEEE 2000 International, 5-7 June 2000 Page(s) 3 - 5
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

EXHIBIT E

Pillsbury Winthrop Shaw Pittman LLP
1600 TYSONS BOULEVARD
MCLEAN, VA 22102

September 12, 2005

James A. Ballanger
703.905.2112
james.ballanger@pillsburylaw.com

Mr. C. Marc Benoit
Hyperchip, Inc.
1800 Rene-Levesque blvd., W.
Montreal, Quebec H3H 2H2
CANADA

Re: **Hyperchip, Inc. – 38700/276792**

Dear Mr. Benoit:

Enclosed herewith you will find correspondence from the United States Department of Commerce, Patent and Trademark Office and\or the client.

Date	Application No.
09/06/05	10/022851

You are advised that Pillsbury Winthrop Shaw Pittman, LLP assumes no further responsibility for these files including, but not limited to, future prosecution and\or annuities. **Please file the appropriate documents to ensure that future correspondence is sent directly to you.**

Sincerely,

James A. Ballanger
Records Transfer Clerk
Northern Virginia Office

Enclosures

30551013v1

EXHIBIT F

PAGE 3547 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

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Mr. C. Marc Benoit
Hyperchip, Inc.
1800 Rene-Levesque Blvd. W.
Montreal, Quebec H3H 2H2
Canada

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EXHIBIT G

UNITED STATES PATENT AND TRADEMARK OFFICE

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United States Patent and Trademark Office
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www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,851	12/20/2001	Claude Thibeault	P 276792.P(US)2001-019	1516

909 7590 04/19/2006

PILLSBURY WINTHROP SHAW PITTMAN, LLP
P.O. BOX 10500
MCLEAN, VA 22102

EXAMINER	
GHULAMALI, QUTBUDDIN	
ART UNIT	**PAPER NUMBER**
2611	

DATE MAILED: 04/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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PTO-90C (Rev. 10/03)

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DEC 01 2009

Notice of Abandonment	Application No.	Applicant(s)
	10/022,851	THIBEAULT ET AL.
	Examiner	Art Unit
	Qutub Ghulamali	2611

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

This application is abandoned in view of:

1. ☒ Applicant's failure to timely file a proper reply to the Office letter mailed on 06 September 2005.
   (a) ☐ A reply was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply (including a total extension of time of _____ month(s)) which expired on _____.
   (b) ☐ A proposed reply was received on _____, but it does not constitute a proper reply under 37 CFR 1.113 (a) to the final rejection.
   (A proper reply under 37 CFR 1.113 to a final rejection consists only of: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114).
   (c) ☐ A reply was received on _____ but it does not constitute a proper reply, or a bona fide attempt at a proper reply, to the non-final rejection. See 37 CFR 1.85(a) and 1.111. (See explanation in box 7 below).
   (d) ☒ No reply has been received.

2. ☐ Applicant's failure to timely pay the required issue fee and publication fee, if applicable, within the statutory period of three months from the mailing date of the Notice of Allowance (PTOL-85).
   (a) ☐ The issue fee and publication fee, if applicable, was received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the statutory period for payment of the issue fee (and publication fee) set in the Notice of Allowance (PTOL-85).
   (b) ☐ The submitted fee of $_____ is insufficient. A balance of $_____ is due.
   The issue fee required by 37 CFR 1.18 is $_____. The publication fee, if required by 37 CFR 1.18(d), is $_____.
   (c) ☐ The issue fee and publication fee, if applicable, has not been received.

3. ☐ Applicant's failure to timely file corrected drawings as required by, and within the three-month period set in, the Notice of Allowability (PTO-37).
   (a) ☐ Proposed corrected drawings were received on _____ (with a Certificate of Mailing or Transmission dated _____), which is after the expiration of the period for reply.
   (b) ☐ No corrected drawings have been received.

4. ☐ The letter of express abandonment which is signed by the attorney or agent of record, the assignee of the entire interest, or all of the applicants.

5. ☐ The letter of express abandonment which is signed by an attorney or agent (acting in a representative capacity under 37 CFR 1.34(a)) upon the filing of a continuing application.

6. ☐ The decision by the Board of Patent Appeals and Interference rendered on _____ and because the period for seeking court review of the decision has expired and there are no allowed claims.

7. ☒ The reason(s) below:

The applicant's representative was called 4/12/2006, 4/13/2006 regarding status of the case that passed the six-month statutory period for reply set forth in the office action mailed on 09/06/2005. No reply received from the applicant

JEAN B. CORRIELUS
PRIMARY EXAMINER
4-17-06

Petitions to revive under 37 CFR 1.137(a) or (b), or requests to withdraw the holding of abandonment under 37 CFR 1.181, should be promptly filed to minimize any negative effects on patent term.

PAGE 38/47 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

EXHIBIT H

Pillsbury
Winthrop
Shaw
Pittman LLP

1650 Tysons Boulevard
McLean, VA 22102-4859

Tel 703.770.7900
Fax 703.770.7901
www.pillsburylaw.com

April 21, 2006

Mr. C. Marc Benoit
Hyperchip, Inc.
1800 Rene-Levesque blvd., W.
Montreal, Quebec H3H 2H2
CANADA

Re: Correspondence relating to Hyperchip, Inc:38700/276792.

Dear Mr. Benoit :

Enclosed are documents relating to the above client, transferred to your firm.

Please ensure the sender of these materials is promptly notified of the transfer to ensure future documents relating to the above matter are sent directly to you. Pillsbury Winthrop Shaw Pittman LLP assumes no further responsibility for the above matter.

Sincerely,

K. Edward Fisher
Facilities and Records Manager

Enclosure

EXHIBIT I

PAGE 42/47 * RCVD AT 12/1/2009 11:05:26 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-5/11 * DNIS:2738300 * CSID: * DURATION (mm-ss):09-20

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Mme C. Marc Benoit
Hyperchip, Inc.
1800 René-Levesque blvd., W.
Montreal, Quebec H3H 2H2
CANADA

650 TYSONS BOULEVARD, McLEAN, VIRGINIA 22102

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File 17273-6US - AD/ad

Montreal, Canada
September 9, 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Richard S. Norman
Serial Number	:	10/022,851
Filed	:	September 16, 2003
Title	:	METHOD AND APPARATUS AND SYSTEM FOR REDUCING INTERFERENCE ON NEARBY CONDUCTORS
Agent of Record	:	Alexandra DAOUD Tel: (514) 847-4333

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450
U.S.A

RESPONSE

Sir:

In response to the Office Action issued on September 6, 2005, please consider the following:

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. – 11. (canceled)

12. (Original) A data transmitter configured and arranged to receive a plurality of input signals and to transmit a plurality of first output signals and a plurality of second output signals, each of the first and second output signals corresponding to a different one of the input signals and each being transmitted along a corresponding one of a plurality of conductive paths, said data transmitter comprising:

a plurality of first latches, each having (1) a clock input configured and arranged to receive a first clock signal including a series of first transitions, with consecutive first transitions being separated by a time period T_CLK, (2) a latch input configured and arranged to receive a corresponding one of the input signals, and (3) an latch output configured and arranged to produce a corresponding latch signal, each first latch being further configured and arranged to latch a data value from the corresponding input signal to the corresponding latch signal upon each first transition; and

a plurality of second latches, each having (1) a clock input configured and arranged to receive a second clock signal based on the first clock signal and including a series of second transitions, with consecutive second transitions being separated by a time period T_CLK, (2) a latch input configured and arranged to receive a corresponding input signal, and (3) a latch output configured and arranged to produce a corresponding latch signal, each second latch being further configured and arranged to latch a data value from the corresponding input signal to the corresponding latch signal upon each second transition;

wherein each first output signal is based on a latch signal of a different one of the first latches and each second output signal is based on a latch signal of a different one of the second latches, and

wherein a time between a transition on an input signal and a corresponding transition on a corresponding second output signal exceeds a time between a transition on an input signal and a corresponding transition on a corresponding first output signal by a delay period T_DLY, and

wherein the time period T_CLK is greater than the delay period T_DLY, and

wherein adjacent conductive paths that each carry one of the plurality of first output signals are separated by a conductive path that carries one of the plurality of second output signals.

13. (Original) The data transmitter according to claim 12, wherein the data transmitter and the plurality of conductive paths are fabricated on the same semiconductor substrate.

14. (Original) The data transmitter according to claim 12, wherein the data transmitter is further configured and arranged to receive an operating voltage from two power rails, and

wherein the two power rails are parallel to and on opposite sides of the plurality of conductive paths.

15. (Original) The data transmitter according to claim 12, wherein each one among the plurality of conductive paths includes a corresponding one of a plurality of parallel transmission lines, and

wherein the data transmitter is further configured and arranged to couple the first clock signal to one of the plurality of parallel transmission lines.

16. (Original) The data transmitter according to claim 12, said data transmitter further comprising a plurality of buffers, each buffer being coupled to a different one of the latch outputs of the first and second latches.

17. (Original) The data transmitter according to claim 12, said data transmitter further comprising a delay element configured and arranged to receive the first clock signal and to produce the second clock signal,

wherein the second clock signal is delayed with respect to the first clock signal by the delay period T_DLY.

18. (Original) The data transmitter according to claim 12, said data transmitter further comprising a plurality of delay elements, each configured and arranged to receive a different one of the second latch signals and to produce the corresponding second output signal.

19. (Original) The data transmitter according to claim 18, wherein the second clock signal is substantially identical to the first clock signal.

20. (Original) The data transmitter according to claim 12, wherein the delay period T_DLY is at least twice as long as a rise time of the data clock signal.

21. – 34. (canceled)

REMARKS

Claim Amendments

Claims 1 to 11 and 21 to 34 have been canceled. Claims 12 to 20 remain in the application.

Claim Rejections

Claims 1 to 11 and 21 to 34 are rejected. These claims have been canceled.

Drawings

The drawings are objected to under 37 CFR 1.83(a) for failing to show state transitions "T1" and "T2" from the specification. The Applicant respectfully submits that all of the elements of the pending claims, namely the recited elements of the claimed data transmitter, are shown in the figures presently on file.

Conclusions

Claims 12 to 20 are said to be allowable over the prior art and a Notice of Allowance is earnestly solicited.

Respectfully submitted,

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